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Application Number	09/611,282
Filing Date	07-06-2000
First Named Inventor	Mohammad A. Abdallah, et al
Group Art Unit	2782
Examiner Name	Not yet assigned
Attorney Docket Number	42390 P9147

*(use as many sheets as necessary)*

Sheet	1	of	2
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Attorney Docket Number 42390.P9147

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

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Examiner  
Signature

W. TREAT

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7/31/03

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

2

of

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### OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials <sup>2</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
WMT		Intel Architecture Optimization Manual, Intel Corporation, Order Number 242816-003, pp. 1-1 to 1-3, 2-1 to 2-16, 3-1 to 3-35, C-1 to C-14, and D-1 to D-2 (1997).
		Intel Architecture Optimization Reference Manual, Intel Corporation, Order Number 245127-001, pp. i-xx, 1-1 to 1-16, 2-1 to 2-31, and C-1 to C-19 (1998-1999).
		Intel Architecture Software Developer's Manual Volume 1: Basic Architecture, Intel Corporation, Order Number 243190, pp. i-xvi, 1-1 to 1-10, 2-1 to 2-14, and 6-1 to 6-46 (1999).
		Intel Architecture Software Developer's Manual Volume 3: System Programming, Intel Corporation, Order Number 243192, pp. i-xxii, 1-1 to 1-9, and 14-1 to 14-35 (1999).
		P6 Family of Processors Hardware Developer's Manual, Intel Corporation, Order Number 244001-001, pp. i-vii, 1-1 to 1-2, and 2-1 to 2-7 (September 1998).
		Pentium® II Processor Developer's Manual, Intel Corporation, Order Number 243502-001, pp. i-x, 1-1 to 1-4, and 2-1 to 2-14 (October 1997).
WMT		U.S. Patent Application No. 09/610,895, filed July 6, 2000, entitled "Hardware Predication for Conditional Instruction Path Branching", by Mohammad A. Abdallah, et al.

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